

**FIGURE 6.2:** Check Sheet for a Manufacturing Process

<b>Printed-Circuit-Board Check Sheet</b> <b>Name of data recorder: JKP</b> <b>Location: Rome, Italy</b> <b>Data collection dates: 04/23 to 04/27</b>						
DEFECT TYPE	DAYS					
	M	Tu	W	Th	F	TOTAL
Misaligned components						11
Adhesive failure						4
Incorrect board dimension						5
Circuitry failure						7
Misaligned mounting holes						8